



PATENT  
54008.8012.US01  
P96-0015US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

**Eric J. BERGMAN**

Serial No.: 09/811,925

Filed: March 19, 2001

For: METHODS FOR CLEANING  
SEMICONDUCTOR SURFACES

Group Art Unit: 1746

Examiner: Z. El-Arini

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TC 1700

**Rule 1.116 Amendment**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the Final Office Action mailed on May 14, 2003, please amend the application as follows:

**IN THE CLAIMS**

Please cancel claim 101 and amend claim 98 as indicated in the attached claim sheets.

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[54008.8012/LA031560.079]



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Re:

U.S. Patent Application Serial No. 09/811,925

Title: Methods for Cleaning Semiconductor Surfaces

Filed: March 19, 2001

Semitool Ref. No.: P96-0015US2

Perkins Coie Ref. No.: 54008.8012.US01

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